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10539334 - GAU: 2825

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10539334
	Filing Date		2005-06-15
	First Named Inventor	Geetha Arthanari, et al.	
	Art Unit	2825	
	Examiner Name	Suchin Parihar	
	Attorney Docket Number	BUR920020005US1	

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/SP/	2	6266803		2001-07-24	Scherer, et al.	
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U.S. PATENT APPLICATION PUBLICATIONS

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/SP/	1	20010028265		2001-10-11	Nitta	
/SP/	2	20010025368		2001-09-27	Cooke, et al.	

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/SP/	3	JP8044776	JP		1996-02-16			<input type="checkbox"/>
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/SP/	2	KEITH M. CARRIG, et al - "A Clock Methodology For High-Performance Microprocessors - Journal of VLSI Signal Processing (1997) - pp. 105-111	<input type="checkbox"/>
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Examiner Signature	/Suchin Parihar/	Date Considered	06/27/2008
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